Pinout FPGA Development Board (DiDeLu WS2011)

Component Pinout

Component	Signal Name	FPGA Pin	I/O Standard
Clock (50 MHz)	clk	PIN_Y2	3.3-V LVTTL
Key 0	res_n	PIN_M23	3.3-V LVTTL
PS/2 Keyboard	ps2_keyboard_clk	PIN_G6	3.3-V LVTTL
	ps2_keyboard_data	PIN_H5	3.3-V LVTTL
Key 1	color_change	PIN_M21	3.3-V LVTTL
Seven Segment Display	seg_data[0]	PIN_G18	2.5 V
	seg_data[1]	PIN_F22	2.5 V
	seg_data[2]	PIN_E17	2.5 V
	seg_data[3]	PIN_L26	3.3-V LVTTL
	seg_data[4]	PIN_L25	3.3-V LVTTL
	seg_data[5]	PIN_J22	3.3-V LVTTL
	seg_data[6]	PIN_H22	3.3-V LVTTL
	seg_data[7]	PIN_M24	3.3-V LVTTL
	seg_data[8]	PIN_Y22	3.3-V LVTTL
	seg_data[9]	PIN_W21	3.3-V LVTTL
	seg_data[10]	PIN_W22	3.3-V LVTTL
	seg_data[11]	PIN_W25	3.3-V LVTTL
	seg_data[12]	PIN_U23	3.3-V LVTTL
	seg_data[13]	PIN_U24	3.3-V LVTTL
RS-232	rx	PIN_G12	3.3-V LVTTL
	tx	PIN_G9	3.3-V LVTTL
VGA	r[0]	PIN_U26	3.3-V LVTTL
	r[1]	PIN_V25	3.3-V LVTTL
	r[2]	PIN_U25	3.3-V LVTTL
	r[3]	PIN_L28	3.3-V LVTTL
	r[4]	PIN_T26	3.3-V LVTTL
	r[5]	PIN_L27	3.3-V LVTTL
	r[6]	PIN_T25	3.3-V LVTTL
	r[7]	PIN_J26	3.3-V LVTTL
	g[0]	PIN_N26	3.3-V LVTTL
	g[1]	PIN_V23	3.3-V LVTTL
	g[2]	PIN_N25	3.3-V LVTTL
	g[3]	PIN_R28	3.3-V LVTTL

	g[4]	PIN_L22	3.3-V LVTTL
	g[5]	PIN_R27	3.3-V LVTTL
	g[6]	PIN_L21	3.3-V LVTTL
	g[7]	PIN_V26	3.3-V LVTTL
	b[0]	PIN_V28	3.3-V LVTTL
	b[1]	PIN_R22	3.3-V LVTTL
	b[2]	PIN_U22	3.3-V LVTTL
	b[3]	PIN_R23	3.3-V LVTTL
	b[4]	PIN_P26	3.3-V LVTTL
	b[5]	PIN_U27	3.3-V LVTTL
	b[6]	PIN_P25	3.3-V LVTTL
	b[7]	PIN_V24	3.3-V LVTTL
	den	PIN_V27	3.3-V LVTTL
	vga_res_n_out	PIN_P28	3.3-V LVTTL
	vga_clk_out	PIN_R21	3.3-V LVTTL
	hsync_n	PIN_P21	3.3-V LVTTL
	vsync_n	PIN_U28	3.3-V LVTTL

Remarks:

- Do not forget to check that the unused pin setting is set to “As Input Tri-Stated with Weak Pull-up” in the device and pin options.
- The nCEO pin must be configured as regular I/O in the device and pin options. (Open Assignments>Device, in the newly opened dialog click Device and Pin Options, in the newly opened dialog select the category Dual Purpose Pins)

Expansion Connector (GPIO)

See Page 49 in the DE2-115 User manual.

The I/O standard for these pins is `3.3-V LVTTL`.