1 General remarks

In all programming exercises, we use IBM's Qiskit system and the Python programming language to construct the quantum circuits, to simulate the quantum machine and to retrieve the results. Example programs are presented in the lecture's tuwel course.

2 General description

The goal is to construct a gate performing integer division. This div gate performs, for all n > 0 and all $m \ge 0$, the division of a non-negative n + m qubit integer a and a positive n qubit integer b and returns the n + m qubit quotient q and the n qubit remainder r such that a = qb + r with $0 \le r < b$. We annotate each register with its length given as a superscript. The div gate consists of 3n + m qubits $d_{3n+m-1} \cdots d_0$. The inputs are $a^{n+m} = d_{n+m-1} \cdots d_1 d_0$, $c^n = d_{2n+m-1} \cdots d_{n+m+1} d_{n+m} = 0 \cdots 0$ and $b^n = d_{3n+m-1} \cdots d_{2n+m+1} d_{2n+m}$. The div gate performs the action

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\left| b^n \right\rangle \left| c^n \right\rangle \left| a^{n+m} \right\rangle \stackrel{\text{div}}{\longmapsto} \left| b^n \right\rangle \left| q^{n+m} \right\rangle \left| r^n \right\rangle.
```

In order to implement the div gate, we provide the implementation of a subtraction (subo) gate with negation qubit and a controlled addition (cadd) gate.

```
A subo gate subo_g can be constructed by
subo_g = subo_gate(qAStart, qASize, qBStart, qBSize, neg).
```

A cadd gate cadd_g can be constructed by

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cadd_g = cadd_gate(qAStart, qASize, qBStart, qBSize, ctrl).
```

For register X, the argument qXStart indicates its start position, qXSize its size and neg/ctrl indicates the position of the negation qubit and control qubit, respectively. Each call returns a gate which can be appended to a given circuit.

We recommend to use the most significant qubit named o for negation/control. The inputs for the gates are then $a = d_{n-1} \cdots d_1 d_0$, $b = d_{2n-1} \cdots d_{n+1} d_n$, and $o = d_{2n}$. The value o changes in the **subo** gate from 0 to 1 if a - b < 0. The actions of these gates are as follows.

 $\begin{array}{c|c} & |0\rangle \left| b \right\rangle \left| a \right\rangle \xrightarrow{\texttt{subo}} \left| o \right\rangle \left| b \right\rangle \left| a - b \right\rangle \\ & |0\rangle \left| b \right\rangle \left| a \right\rangle \xrightarrow{\texttt{cadd}} \left| 0 \right\rangle \left| b \right\rangle \left| a \right\rangle & |1\rangle \left| b \right\rangle \left| a \right\rangle \xrightarrow{\texttt{cadd}} \left| 1 \right\rangle \left| b \right\rangle \left| a + b \right\rangle \end{array}$

3 Your tasks

We guide you step by step through the implementation process.

 Construct a gate iter which will be used later on. This gate first subtracts with negation qubit the n qubit non-negative integer b from the n qubit non-negative integer a using the subtraction gate subo. In case the negation qubit o is 1, the difference is negative and we add b back. This is performed by a controlled addition with gate cadd, using the negation qubit o from the subo gate as the control qubit of the cadd gate. Finally the negation/control qubit is negated.

The inputs of the iter gate are $a = d_{n-1} \cdots d_1 d_0$, $b = d_{2n-1} \cdots d_{n+1} d_n$, and negation/control qubit $o = d_{2n} = 0$. It performs the action

$(a-b) \ge 0$	(a-b) < 0
$\left 0 ight angle\left b ight angle\left a ight angle\stackrel{\texttt{iter}}{\longmapsto}\left 1 ight angle\left b ight angle\left a-b ight angle$	$\left 0 ight angle\left b ight angle\left a ight angle\stackrel{\texttt{iter}}{\longmapsto}\left 0 ight angle\left b ight angle\left a ight angle$

Implement the gate for later use. Draw the gate for n = 3, test your implementation for all possibilities over n = 2 qubits and document the test cases.

2. Construct the div gate using the iter gate.

d_0	-	0	-		d_0	-		_		_		_		-	0	-	
d_1	-	1	-		d_1	-						_	0	-	1	-	
d ₂	-	2	-		d ₂	-				_	0	-	1	-	2	-	
d₃	-	3	-		d ₃	-		_	0	-	1	_	2	-	6	-	
d4	-	4	-		d_4	-	0	-	1	-	2	_	6	-		-	
d5	-	5div	-	=	d_5	-	1	-	2	-	6	_	iter	-	iter	-	
d ₆	-	6	-		d_6	-	2	-	6	-	iter	_	iter	-		-	
d7	-	7	-		<i>d</i> ₇	-	6iter	-	iter	-		_		-		-	
d ₈	-	8	-		d ₈	-	3	-	3	-	3	_	3	-	3	-	
d9	-	9	-		d9	-	4	-	4	-	4	_	4	-	4	-	
d ₁₀	-	10	-		<i>d</i> ₁₀	-	5	-	5	-	5	_	5	_	5	-	

The example circuit for n = 3 and m = 2 is given on the left. Let $a^{n+m} = d_4 d_3 d_2 d_1 d_0 = 11101$ and let $b^n = d_{10} d_9 d_8 = 111$. Figure out the input and output of each iter gate and document the findings in a table. What is q and r for this example? Test your implementation for various n and m and document the test cases.

3. Suppose we want to invert an n qubit number b. How can we do this using the div gate? How does m influence the precision of the inversion process? Invert $b^4 = 1011$ and discuss the result.