

Formal Semantics of Programming Languages

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The language *While*

$E \in \textit{Arith} ::= x \mid n \mid E + E \mid E * E \mid \dots$

$B \in \textit{Bool} ::= \text{true} \mid \text{false} \mid E = E \mid E \leq E$
 $\mid B \wedge B \mid \neg B$

$C \in \textit{Com} ::= x := E \mid \text{if } B \text{ then } C \text{ else } C \mid C ; C$
 $\mid \text{skip} \mid \text{while } B \text{ do } C$

x is taken from some set of variables *Var*

Provably Correct Implementation

The formal specification of the semantics of a programming language allows to argue about the correctness of a compiler:

- We define an **abstract machine** (e.g. a **stack-based** intermediate language such as *Java bytecode*).
- We define small-step semantics for this machine.
- We define a **translation** of *While* into assembly code for the abstract machine.
- We prove that code translation and execution on the abstract machine are **semantics preserving** for every command of *While*.

The *Abstract Machine (AM)*

$\text{inst} \in \text{Inst} ::= \text{PUSH-n} \mid \text{ADD} \mid \text{MULT} \mid$
 $\mid \text{TRUE} \mid \text{FALSE} \mid \text{EQ} \mid \text{LE} \mid \text{AND} \mid \text{NEG}$
 $\mid \text{FETCH-x} \mid \text{STORE-x} \mid \text{NOOP}$
 $\mid \text{BRANCH}(c,c) \mid \text{LOOP}(c,c)$

$c \in \text{Code} ::= \epsilon \mid \text{inst}:c$

AM Configurations

AM has configurations $\langle c, e, s \rangle$:

- c is the **code** (sequence of instructions) to be executed,
- e is the **evaluation stack**,
- s is the **storage**.

We have $e \in Stack = (\mathbb{Z} \cup \mathbb{T})^*$,
where $\mathbb{T} = \{\text{true}, \text{false}\}$.

For simplicity we assume $s \in State$.

Thus $\langle c, e, s \rangle \in Code \times Stack \times State$.

Small-step Semantics of *AM*

Judgements:

$\langle c, e, s \rangle \triangleright \langle c', e', s' \rangle$

Meaning:

One step of execution transforms a configuration $\langle c, e, s \rangle$ into $\langle c', e', s' \rangle$.

Terminal configurations:

A configuration is terminal, if its code component is the empty sequence: $\langle \epsilon, e, s \rangle$

Small-step Semantics of *AM*

$\langle \text{PUSH-}n:c,e,s \rangle$	\triangleright	$\langle c, \llbracket n \rrbracket : e, s \rangle$
$\langle \text{ADD}:c,z_1:z_2:e,s \rangle$	\triangleright	$\langle c, (z_1 + z_2) : e, s \rangle$, if $z_1, z_2 \in \mathbb{Z}$
$\langle \text{MULT}:c,z_1:z_2:e,s \rangle$	\triangleright	$\langle c, (z_1 * z_2) : e, s \rangle$, if $z_1, z_2 \in \mathbb{Z}$
$\langle \text{TRUE}:c,e,s \rangle$	\triangleright	$\langle c, \text{true} : e, s \rangle$
$\langle \text{FALSE}:c,e,s \rangle$	\triangleright	$\langle c, \text{false} : e, s \rangle$
$\langle \text{EQ}:c,z_1:z_2:e,s \rangle$	\triangleright	$\langle c, (z_1 = z_2) : e, s \rangle$, if $z_1, z_2 \in \mathbb{Z}$
$\langle \text{LE}:c,z_1:z_2:e,s \rangle$	\triangleright	$\langle c, (z_1 \leq z_2) : e, s \rangle$, if $z_1, z_2 \in \mathbb{Z}$
$\langle \text{AND}:c,t_1:t_2:e,s \rangle$	\triangleright	$\langle c, (t_1 \wedge t_2) : e, s \rangle$, if $t_1, t_2 \in \mathbb{T}$
$\langle \text{NEG}:c,t:e,s \rangle$	\triangleright	$\langle c, (\neg t) : e, s \rangle$, if $t \in \mathbb{T}$

Small-step Semantics of *AM*

$\langle \text{FETCH-}x:c,e,s \rangle \triangleright \langle c,s(x):e,s \rangle$

$\langle \text{STORE-}x:c,z:e,s \rangle \triangleright \langle c,e,s[x \mapsto z] \rangle$

$\langle \text{NOOP}:c,e,s \rangle \triangleright \langle c,e,s \rangle$

$\langle \text{BRANCH}(c_1,c_2):c,t:e,s \rangle \triangleright \begin{cases} \langle c_1:c,e,s \rangle, & \text{if } t=\text{true} \\ \langle c_2:c,e,s \rangle, & \text{if } t=\text{false} \end{cases}$

$\langle \text{LOOP}(c_1,c_2):c,e,s \rangle \triangleright \langle c_1:\text{BRANCH}(c_2:\text{LOOP}(c_1,c_2), \text{NOOP}:c),e,s \rangle$

Example

We assume $s(x) = 3$.

$\langle \text{PUSH-1:FETCH-x:ADD:STORE-x}, \epsilon, s \rangle$

▷ $\langle \text{FETCH-x:ADD:STORE-x}, 1, s \rangle$

▷ $\langle \text{ADD:STORE-x}, 3:1, s \rangle$

▷ $\langle \text{STORE-x}, 4, s \rangle$

▷ $\langle \epsilon, \epsilon, s[x \mapsto 4] \rangle$

Non-termination

$\langle \text{LOOP}(\text{TRUE}, \text{NOOP}), \epsilon, s \rangle$

▷ $\langle \text{TRUE}:\text{BRANCH}(\text{NOOP}:\text{LOOP}(\text{TRUE}, \text{NOOP}), \text{NOOP}), \epsilon, s \rangle$

▷ $\langle \text{BRANCH}(\text{NOOP}:\text{LOOP}(\text{TRUE}, \text{NOOP}), \text{NOOP}), \text{true}, s \rangle$

▷ $\langle \text{NOOP}:\text{LOOP}(\text{TRUE}, \text{NOOP}), \text{true}, s \rangle$

▷ $\langle \text{LOOP}(\text{TRUE}, \text{NOOP}), \text{true}, s \rangle$

▷ ...

Properties of AM

Lemma:

If $\langle c_1:c_2, e, s \rangle \rightarrow^k \langle \epsilon, e', s' \rangle$ then there exists a configuration $\langle \epsilon, e'', s'' \rangle$ and natural numbers k_1 and k_2 such that $\langle c_1, e, s \rangle \rightarrow^{k_1} \langle \epsilon, e'', s'' \rangle$ and $\langle c_2, e'', s'' \rangle \rightarrow^{k_2} \langle \epsilon, e', s' \rangle$ where $k_1 + k_2 = k$.

Lemma:

If $\langle c, e, s \rangle \triangleright^k \langle c', e', s' \rangle$ then $\langle c_1:c_2, e_1:e_2, s \rangle \triangleright^k \langle c':c_2, e':e_2, s' \rangle$

Determinacy:

If $\langle c, e, s \rangle \triangleright^* \gamma_1$ and $\langle c, e, s \rangle \rightarrow^* \gamma_2$ then $\gamma_1 = \gamma_2$.

Stuck Configurations

AM has stuck configurations:

- $\langle \text{ADD}, \text{true}:10, s \rangle$
- $\langle \text{NEG}, 5, s \rangle$
- ...

These configurations arise because of **type errors!** (We could add error states to the abstract machine configurations...)

The Meaning of Commands

$\llbracket - \rrbracket_{AM}: Code \rightarrow States \rightarrow States$

$\llbracket c \rrbracket_{AM}$ transforms an initial state s into a final (aka terminal) state

Definition:

$$\llbracket c \rrbracket_{AM}(s) = \begin{cases} s' & \text{if } \langle c, \epsilon, s \rangle \rightarrow^* \langle \epsilon, e, s' \rangle \\ \perp & \text{otherwise} \end{cases}$$

Determinacy ensures this is proper definition.

\perp stands for 'undefined'.

Translation of *While* to *AM*

We define three (total) functions that translate *While* commands into *AM* code:

$$CA[\]: Arith \rightarrow Code$$
$$CB[\]: Bool \rightarrow Code$$
$$CC[\]: Com \rightarrow Code$$

We will define these function in a **compositional manner** (i.e. by structural induction).

Translation of Arithmetic Expressions

$$\begin{aligned} \mathcal{CA}[n] &= \text{PUSH-}n \\ \mathcal{CA}[x] &= \text{FETCH-}x \\ \mathcal{CA}[E_1 + E_2] &= \mathcal{CA}[E_2]:\mathcal{CA}[E_1]:\text{ADD} \\ \mathcal{CA}[E_1 * E_2] &= \mathcal{CA}[E_2]:\mathcal{CA}[E_1]:\text{MULT} \end{aligned}$$

Translation of Boolean expressions is defined similarly.

Translation of Commands

$$\begin{aligned} \mathcal{CC}[\mathit{x} := \mathit{E}] &= \mathcal{CA}[\mathit{E}]:\text{STORE-}\mathit{x} \\ \mathcal{CC}[\text{skip}] &= \text{NOOP} \\ \mathcal{CC}[\mathit{C}_1; \mathit{C}_2] &= \mathcal{CC}[\mathit{C}_1]:\mathcal{CC}[\mathit{C}_2] \\ \mathcal{CC}[\text{if } \mathit{B} \text{ then } \mathit{C}_1 \text{ else } \mathit{C}_2] &= \mathcal{CB}[\mathit{B}]: \\ &\quad \text{BRANCH}(\mathcal{CC}[\mathit{C}_1], \mathcal{CC}[\mathit{C}_2]) \\ \mathcal{CC}[\text{while } \mathit{B} \text{ do } \mathit{C}] &= \text{LOOP}(\mathcal{CB}[\mathit{B}], \mathcal{CC}[\mathit{C}]) \end{aligned}$$

Note that the definition is compositional.

This guarantees the termination of the translation!

Example

$$\begin{aligned} & \mathcal{CC}[\![y := 1; \text{while } \neg(x=1) \text{ do } (y := y * x; x := x - 1) \!]\!] = \\ & \mathcal{CC}[\![y := 1]\!]:\mathcal{CC}[\![\text{while } \neg(x=1) \text{ do } (y := y * x; x := x - 1) \!]\!] = \\ & \mathcal{CA}[\![1]\!]:\text{STORE-}y:\text{LOOP}(\mathcal{CB}[\![\neg(x=1)]\!],\mathcal{CC}[\![y := y * x; x := x - 1]\!]) = \\ & \text{PUSH-1:STORE-}y:\text{LOOP}(\mathcal{CB}[\![x=1]\!]:\text{NEG},\mathcal{CC}[\![y := y * x]\!]: \\ & \qquad \qquad \qquad \qquad \qquad \qquad \qquad \mathcal{CC}[\![x := x - 1]\!]) = \dots = \\ & \text{PUSH-1:STORE-}y:\text{LOOP}(\text{FETCH-}x:\text{PUSH-1:EQ:NEG},\text{FETCH-}x: \\ & \text{FETCH-}y:\text{MULT:STORE-}y:\text{FETCH-}x:\text{PUSH-1:SUB:STORE-}y) \end{aligned}$$

Correctness of Arithmetic Expressions

We show that first translating an arithmetic expression into code for *AM* and then executing gives the same result as the semantics of *While*.

Lemma

For all arithmetic expressions E we have
$$\langle \mathcal{CA}[[E]], \epsilon, s \rangle \triangleright^* \langle \epsilon, [[E]] s, s \rangle.$$

Furthermore, all intermediate configurations of this computation have non-empty evaluation stacks.

$$\langle \mathcal{CA}[[E]], \epsilon, s \rangle \triangleright^* \langle \epsilon, [[E]] s, s \rangle$$

Proof By structural induction on the expression E.

Base Case: E is a numeral n. We have $\mathcal{CA}[[E]] = \text{PUSH-n}$ and we get $\langle \text{PUSH-n}, \epsilon, s \rangle \triangleright \langle \epsilon, [[n]] s, s \rangle$.

This solves the case.

Base Case: E is a variable x. We have $\mathcal{CA}[[x]] = \text{FETCH-x}$ and we get $\langle \text{FETCH-x}, \epsilon, s \rangle \triangleright \langle \epsilon, s(x), s \rangle$.

This solves the case.

$$\langle \mathcal{CA}[[E]], \epsilon, s \rangle \triangleright^* \langle \epsilon, [[E]] s, s \rangle$$

Induction Case:

Suppose E is of the form $(E_1 + E_2)$. (case $E_1 * E_2$ for is analogous)

We have $\mathcal{CA}[[E_1 + E_2]] = \mathcal{CA}[[E_2]] : \mathcal{CA}[[E_1]] : \text{ADD}$.

From the induction hypothesis we get

$$\langle \mathcal{CA}[[E_1]], \epsilon, s \rangle \triangleright^* \langle \epsilon, [[E_1]] s, s \rangle \text{ and } \langle \mathcal{CA}[[E_2]], \epsilon, s \rangle \triangleright^* \langle \epsilon, [[E_2]] s, s \rangle.$$

In both cases the intermediate configurations have non-empty evaluation stacks.

$$\begin{aligned} \text{From lemma on slide 11 we get } & \langle \mathcal{CA}[[E_2]] : \mathcal{CA}[[E_1]] : \text{ADD}, \epsilon, s \rangle \triangleright^* \\ & \langle \mathcal{CA}[[E_1]] : \text{ADD}, [[E_2]] s, s \rangle \triangleright^* \langle \text{ADD}, ([[E_1]] s) : ([[E_2]] s), s \rangle. \end{aligned}$$

$$\begin{aligned} \text{Furthermore we have } & \langle \text{ADD}, ([[E_1]] s) : ([[E_2]] s), s \rangle \triangleright \\ & \langle \epsilon, ([[E_1]] s + [[E_2]] s), s \rangle. \end{aligned}$$

Since $[[E_1]] s + [[E_2]] s = [[E_1 + E_2]] s$ we get the desired result.

An Equivalence Result

Theorem

For all commands C we have $\llbracket C \rrbracket_B = \llbracket \mathcal{CC}[C] \rrbracket_{AM}$.

Proof

We split the proof into the two lemmas for the cases

$\langle \mathcal{CC}[C], \epsilon, s \rangle \triangleright^k \langle \epsilon, e, s' \rangle$ implies $\langle C, s \rangle \Downarrow s'$ and $e = \epsilon$

and

$\langle C, s \rangle \Downarrow s'$ implies $\langle \mathcal{CC}[C], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$.

$$\langle C, s \rangle \Downarrow s' \text{ implies } \langle \mathcal{CC}[[C]], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$$

The proof proceeds by rule induction on $\langle C, s \rangle \Downarrow s'$.

Case B-ASS:

We assume $\langle x := E, s \rangle \Downarrow s[x \mapsto [[E]] s]$.

We have $\mathcal{CC}[[x := E]] = \mathcal{CA}[[E]]:\text{STORE-}x$.

The previous lemma gives us $\langle \mathcal{CA}[[E]], \epsilon, s \rangle \triangleright^* \langle \epsilon, [[E]] s, s \rangle$.

According to the lemma on slide 11 we have

$$\begin{aligned} \langle \mathcal{CA}[[E]]:\text{STORE-}x, \epsilon, s \rangle &\triangleright^* \langle \text{STORE-}x, [[E]] s, s \rangle \\ &\triangleright \langle \epsilon, \epsilon, s[x \mapsto [[E]] s] \rangle. \end{aligned}$$

Case B-SKIP: Straightforward.

$$\langle C, s \rangle \Downarrow s' \text{ implies } \langle \mathcal{CC}[C], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$$

Case B-SEQ:

We have $\mathcal{CC}[C_1 ; C_2] = \mathcal{CC}[C_1] : \mathcal{CC}[C_2]$.

We assume $\langle C_1 ; C_2, s \rangle \Downarrow s'$ has been derived from $\langle C_1, s \rangle \Downarrow s''$ and $\langle C_2, s'' \rangle \Downarrow s'$.

The induction hypothesis can be applied to both premises $\langle C_1, s \rangle \Downarrow s''$ and $\langle C_2, s'' \rangle \Downarrow s'$.

This gives us $\langle \mathcal{CC}[C_1], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s'' \rangle$ and $\langle \mathcal{CC}[C_2], \epsilon, s'' \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$.

According to the lemma on slide 11 we have $\langle \mathcal{CC}[C_1] : \mathcal{CC}[C_2], \epsilon, s \rangle \triangleright^* \langle \mathcal{CC}[C_2], \epsilon, s'' \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$.

Thus $\langle \mathcal{CC}[C], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$.

$\langle C, s \rangle \Downarrow s'$ implies $\langle \mathcal{CC}[[C]], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$

Case B-IF.T:

We assume $\langle \text{if } B \text{ then } C_1 \text{ else } C_2, s \rangle \Downarrow s'$ has been derived from $\langle C_1, s \rangle \Downarrow s'$ and $\llbracket B \rrbracket s = \text{true}$.

From the induction hypothesis we get $\langle \mathcal{CC}[[C_1]], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$.

We have $\mathcal{CC}[\text{if } B \text{ then } C_1 \text{ else } C_2] =$
 $\mathcal{CB}[[B]]: \text{BRANCH}(\mathcal{CC}[[C_1]], \mathcal{CC}[[C_2]])$.

We get $\langle \mathcal{CB}[[B]]: \text{BRANCH}(\mathcal{CC}[[C_1]], \mathcal{CC}[[C_2]]), \epsilon, s' \rangle \triangleright^*$
 $\langle \text{BRANCH}(\mathcal{CC}[[C_1]], \mathcal{CC}[[C_2]]), \llbracket B \rrbracket s, s' \rangle$ using a lemma for Boolean expressions (similar to the lemma for arithmetic expressions on slide 18) and the lemma from slide 11.

Finally we have $\langle \text{BRANCH}(\mathcal{CC}[[C_1]], \mathcal{CC}[[C_2]]), \llbracket B \rrbracket s, s' \rangle \triangleright$
 $\langle \mathcal{CC}[[C_1]], \epsilon, s \rangle$ from the small-step semantics of *AM*.

Case B-IF.F: Analogous.

$$\langle C, s \rangle \Downarrow s' \text{ implies } \langle \mathcal{CC}[\![C]\!], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$$

Case B-WHILE.T:

We assume $\langle \text{while } B \text{ do } C, s \rangle \Downarrow s'$ has been derived from $\langle C, s \rangle \Downarrow s''$, $\langle \text{while } B \text{ do } C, s'' \rangle \Downarrow s'$ and $\llbracket B \rrbracket s = \text{true}$.

From the induction hypothesis to $\langle C, s \rangle \Downarrow s''$ we get $\langle \mathcal{CC}[\![C]\!], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s'' \rangle$ and $\langle \mathcal{CC}[\![\text{while } B \text{ do } C]\!], \epsilon, s'' \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$.

We have $\mathcal{CC}[\![\text{while } B \text{ do } C]\!] = \text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!])$.

We get $\langle \text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \epsilon, s \rangle \triangleright$

$\langle \mathcal{CB}[\![B]\!]:\text{BRANCH}(\mathcal{CC}[\![C]\!]:\text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \text{NOOP}), \epsilon, s \rangle \triangleright^*$

$\langle \text{BRANCH}(\mathcal{CC}[\![C]\!]:\text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \text{NOOP}), \mathcal{CB}[\![B]\!] s, s \rangle \triangleright$

$\langle \mathcal{CC}[\![C]\!]:\text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \epsilon, s \rangle \triangleright^* \langle \text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \epsilon, s'' \rangle$.

In the second step we have used a for Boolean expressions (similar to the lemma for arithmetic expressions on slide 18). In the last step we have used the lemma from slide 11.

Case B-WHILE.F: Straightforward.

$\langle \mathcal{CC}[\![C]\!], \epsilon, s \rangle \triangleright^k \langle \epsilon, e, s' \rangle$ implies $\langle C, s \rangle \Downarrow s'$ and $e = \epsilon$

The proof proceeds by induction on the length of the derivation sequence $\langle \mathcal{CC}[\![C]\!], \epsilon, s \rangle \triangleright^k \langle \epsilon, e, s' \rangle$, that is by induction on k .

Induction hypothesis: We assume the lemma holds for all $0 \leq k' \leq k$. We proceed by case distinction on the first step of $\langle \mathcal{CC}[\![C]\!], \epsilon, s \rangle \triangleright^{k+1} \langle \epsilon, e, s' \rangle$.

S-SKIP: Straightforward.

$\langle \mathcal{CC}[\![C]\!], \epsilon, s \rangle \triangleright^k \langle \epsilon, e, s' \rangle$ implies $\langle C, s \rangle \Downarrow s'$ and $e = \epsilon$

Case $x := E$:

We assume $\langle \mathcal{CA}[\![E]\!]:\text{STORE-}x, \epsilon, s \rangle \triangleright^{k+1} \langle \epsilon, e, s' \rangle$.

According to the lemma on slide 11 there must be a configuration $\langle \epsilon, e'', s'' \rangle$ and natural numbers k_1 and k_2 such that $\langle \mathcal{CA}[\![E]\!], \epsilon, s \rangle \rightarrow^{k_1} \langle \epsilon, e'', s'' \rangle$ and

$\langle \text{STORE-}x, e'', s'' \rangle \rightarrow^{k_2} \langle \epsilon, e, s' \rangle$ where $k_1 + k_2 = k+1$.

Due to the lemma on slide 18 and due to the determinacy of AM we have $e'' = \llbracket E \rrbracket s$ and $s'' = s$.

By the semantics of $\text{STORE-}x$ we get

$$s' = s[x \mapsto \llbracket E \rrbracket s] \text{ and } e = \epsilon.$$

From B-ASS we get $\langle x := E, s \rangle \Downarrow s[x \mapsto \llbracket E \rrbracket s]$.

$\langle \mathcal{CC}[\![C]\!], \epsilon, s \rangle \triangleright^k \langle \epsilon, e, s' \rangle$ implies $\langle C, s \rangle \Downarrow s'$ and $e = \epsilon$

Case $C_1 ; C_2$:

We assume $\mathcal{CC}[\![C_1]\!]:\mathcal{CC}[\![C_2]\!] \triangleright^{k+1} \langle \epsilon, e, s' \rangle$.

According to the lemma on slide 11 there must be a configuration $\langle \epsilon, e'', s'' \rangle$ and natural numbers k_1 and k_2 such that $\langle \mathcal{CC}[\![C_1]\!], \epsilon, s \rangle \triangleright^{k_1} \langle \epsilon, e'', s'' \rangle$ and $\langle \mathcal{CC}[\![C_2]\!], e'', s'' \rangle \triangleright^{k_2} \langle \epsilon, e, s' \rangle$ where $k_1 + k_2 = k+1$.

Because of $k_1 \leq k$ the induction hypothesis can be applied and we get $\langle C_1, s \rangle \Downarrow s''$ and $e'' = \epsilon$.

Now we can apply the induction hypothesis again because of $k_2 \leq k$ and we get $\langle C_2, s'' \rangle \Downarrow s'$ and $e = \epsilon$.

From B-SEQ we get $\langle C_1 ; C_2, s \rangle \Downarrow s'$.

$\langle \mathcal{CC}[[C]], \epsilon, s \rangle \triangleright^k \langle \epsilon, e, s' \rangle$ implies $\langle C, s \rangle \Downarrow s'$ and $e = \epsilon$

Case if B then C_1 else C_2 :

We assume $\langle \mathcal{CB}[[B]]:\text{BRANCH}(\mathcal{CC}[[C_1]], \mathcal{CC}[[C_2]]), \epsilon, s \rangle \triangleright^{k+1} \langle \epsilon, e, s' \rangle$.

According to the lemma on slide 11 there must be a configuration $\langle \epsilon, e'', s'' \rangle$ and natural numbers k_1 and k_2 such that $\langle \mathcal{CB}[[B]], \epsilon, s \rangle \triangleright^{k_1} \langle \epsilon, e'', s'' \rangle$ and $\langle \text{BRANCH}(\mathcal{CC}[[C_1]], \mathcal{CC}[[C_2]]), e'', s'' \rangle \triangleright^{k_2} \langle \epsilon, e, s' \rangle$ where $k_1 + k_2 = k+1$.

Due to a lemma similar to the one on slide 18 and due to the determinacy of AM we have $e'' = [[B]] s$ and $s'' = s$.

From now on we assume $[[B]] s = \text{true}$.

Thus $\langle \text{BRANCH}(\mathcal{CC}[[C_1]], \mathcal{CC}[[C_2]]), [[B]] s, s \rangle \triangleright \langle \mathcal{CC}[[C_1]], \epsilon, s \rangle \triangleright^{k_2-1} \langle \epsilon, e, s' \rangle$ by determinacy of AM .

Because of $k_2-1 \leq k$ the induction hypothesis can be applied and we get $\langle C_1, s \rangle \Downarrow s''$ and $e' = \epsilon$.

From B-IF.T we get $\langle \text{if B then } C_1 \text{ else } C_2, s \rangle \Downarrow s'$.

The case $[[B]] s = \text{false}$ is analogous.

$\langle \mathcal{CC}[\![C]\!], \epsilon, s \rangle \triangleright^k \langle \epsilon, e, s' \rangle$ implies $\langle C, s \rangle \Downarrow s'$ and $e = \epsilon$

Case while B do C:

We assume $\langle \text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \epsilon, s \rangle \triangleright^{k+1} \langle \epsilon, e, s' \rangle$.

Using the semantics of AM we have

$\langle \text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \epsilon, s \rangle \triangleright$
 $\langle \mathcal{CB}[\![B]\!]:\text{BRANCH}(\mathcal{CC}[\![C]\!]:\text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \text{NOOP}), \epsilon, s \rangle \triangleright^k$
 $\langle \epsilon, e, s' \rangle$.

According to the lemma on slide 11 there must be a configuration $\langle \epsilon, e'', s'' \rangle$ and natural numbers k_1 and k_2 such that $\langle \mathcal{CB}[\![B]\!], \epsilon, s \rangle \triangleright^{k_1} \langle \epsilon, e'', s'' \rangle$ and $\langle \text{BRANCH}(\mathcal{CC}[\![C]\!]:\text{LOOP}(\mathcal{CB}[\![B]\!], \mathcal{CC}[\![C]\!]), \text{NOOP}), e'', s'' \rangle \triangleright^{k_2} \langle \epsilon, e, s' \rangle$ where $k_1 + k_2 = k + 1$.

Due to a lemma similar to the one on slide 18 and due to the determinacy of AM we have $e'' = \llbracket B \rrbracket s$ and $s'' = s$.

$\langle \mathcal{CC}[[C]], \epsilon, s \rangle \triangleright^k \langle \epsilon, e, s' \rangle$ implies $\langle C, s \rangle \Downarrow s'$ and $e = \epsilon$

Case $[[B]] s = \text{true}$:

Thus $\langle \text{BRANCH}(\mathcal{CC}[[C]]:\text{LOOP}(\mathcal{CB}[[B]], \mathcal{CC}[[C]]), \text{NOOP}), [[B]] s, s \rangle \triangleright \langle \mathcal{CC}[[C]]:\text{LOOP}(\mathcal{CB}[[B]], \mathcal{CC}[[C]]), \epsilon, s \rangle \triangleright^{k_2-1} \langle \epsilon, e, s' \rangle$.

Because $\mathcal{CC}[[C]]:\text{LOOP}(\mathcal{CB}[[B]], \mathcal{CC}[[C]]) = \mathcal{CC}[[C; \text{while } B \text{ do } C]]$ and $k_2-1 \leq k$ we can apply the induction hypothesis and get $\langle C; \text{while } B \text{ do } C, s \rangle \Downarrow s'$ and $e = \epsilon$.

From B-SEQ we get $\langle C, s \rangle \Downarrow s''$ and $\langle \text{while } B \text{ do } C, s'' \rangle \Downarrow s'$ for some state s'' .

From B-WHILE.T we get $\langle \text{while } B \text{ do } C, s \rangle \Downarrow s'$.

Case $[[B]] s = \text{false}$:

We have $\langle \text{BRANCH}(\mathcal{CC}[[C]]:\text{LOOP}(\mathcal{CB}[[B]], \mathcal{CC}[[C]]), \text{NOOP}), [[B]] s, s \rangle \triangleright \langle \text{NOOP}, \epsilon, s \rangle \triangleright^{k_2-1} \langle \epsilon, e, s' \rangle$ and thus $e = \epsilon$ and $s = s'$.

From B-WHILE.F we get $\langle \text{while } B \text{ do } C, s \rangle \Downarrow s'$.

Comment on the Proof

- Proof is very similar to the equivalence proof for the small-step and big-step semantics of *While*.
- Clearly we also have $\llbracket C \rrbracket_s = \llbracket CC[C] \rrbracket_{AM}$ because of this equivalence!

Question: We defined small-step semantics for AM, so why didn't we prove $\llbracket C \rrbracket_s = \llbracket CC[C] \rrbracket_{AM}$??

Alternative Proof Technique

We define a bisimulation relation \equiv between the configurations of the small-steps semantics of *AM* and *While*:

$$\langle C, s \rangle \equiv \langle \mathcal{CC}[[C]], \epsilon, s \rangle \text{ for all commands } C$$

$$s \equiv \langle \epsilon, \epsilon, s \rangle$$

The idea is that only certain configuration in *AM* correspond to configurations of *While*.

Easy Direction

We could try to show that if

$$\gamma_S \equiv \gamma_{AM} \text{ and } \gamma_S \rightarrow \gamma_S'$$

then exists a configuration γ_{AM}' such that

$$\gamma_{AM} \triangleright^{\geq 1} \gamma_{AM}' \text{ and } \gamma_S' \equiv \gamma_{AM}'.$$

This guarantees that if $\langle C, s \rangle \rightarrow^* s'$ then $\langle CC[C], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$.

Difficult Direction

Assume that $\gamma_S \equiv \gamma_{AM}^1$ and

$$\gamma_{AM}^1 \triangleright \gamma_{AM}^2 \triangleright \dots \triangleright \gamma_{AM}^k,$$

where $k > 1$ and only γ_{AM}^1 and γ_{AM}^k **have empty evaluation stacks**, i.e., they are of the form $\langle c, \epsilon, s' \rangle$.

We could try to show that there exists a configuration γ_S' such that

$$\gamma_S \rightarrow \gamma_S' \text{ and } \gamma_S' \equiv \gamma_{AM}^k.$$

This guarantees that if $\langle CC[[C]], \epsilon, s \rangle \triangleright^* \langle \epsilon, \epsilon, s' \rangle$ then $\langle C, s \rangle \rightarrow^* s'$.

Difficulties

- Difficult direction: relies on the fact, that if AM moves from some configuration with an empty stack to another configuration with an empty stack, this can be imitated by one step of the small-step semantics of While. (Consider for example our assumption that expressions are evaluated in one step).
- The proof relies on the two semantics proceeding in **lock-step**: we need to find configurations in the two derivation sequences that correspond to one another. Often this is not possible and one has to raise the level of abstraction for one of the semantics. This is exactly what happens when the small-step semantics is replaced by the big-step semantics (we do not care about the individual computation steps but only about the result).

Difficulties: Example

The difficult direction goes through when we use the rule:

$$\text{S-WHILE} \frac{}{\langle \text{while } B \text{ do } C, s \rangle \rightarrow \langle \text{if } B \text{ then } C; \text{while } B \text{ do } C \text{ else skip}, s \rangle}$$

Does it complicate the proof if we use the alternative rules???

$$\text{S-WHILE.F} \frac{}{\langle \text{while } B \text{ do } C, s \rangle \rightarrow s} \quad \llbracket B \rrbracket s = \text{false}$$

$$\text{S-WHILE.T} \frac{}{\langle \text{while } B \text{ do } C, s \rangle \rightarrow \langle C; \text{while } B \text{ do } C, s \rangle} \quad \llbracket B \rrbracket s = \text{true}$$